



FORM PTO-1449	SERIAL NO. 10/023,200	CASE NO. 10519/24
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE December 14, 2001	GROUP ART UNIT 2818
(use several sheets if necessary)		APPLICANT(S): MARCH ET AL.

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
pmc	A17 WO 99/14763	03/25/99	PCT		

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
pmc	A23	"Three-Dimensional Memory Array and Method of Fabrication," U.S. Patent Application Serial No. 09/560,626, filed April 28, 2000; inventor: Johan Knall.
	A24	"Write-Once Memory Array Controller, System, and Method," U.S. Patent Application Serial No. 09/638,427, filed August 14, 2000; inventors: Derek J. Bosch, Christopher S. Moore, Daniel C. Steere, and J. James Tringali.
	A25	"Low-Cost Three-Dimensional Memory Array," U.S. Patent Application Serial No. 09/638,428, filed August 14, 2000; inventors: Mark G. Johnson, Thomas H. Lee, Vivek Subramanian, and P. Michael Farmwald.
	A26	"Modular Memory Device," U.S. Patent Application Serial No. 09/638,334, filed August 14, 2000; inventors: J. James Tringali, P. Michael Farmwald, Thomas H. Lee, Mark G. Johnson, and Derek J. Bosch.
	A27	"Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. 09/748,589, filed December 22, 2000; inventors: Roger W. March, Christopher S. Moore, Daniel Brown, Thomas H. Lee, and Mark G. Johnson.
	A28	"Three-Dimensional Memory Array and Method for Storing Data Bits and ECC Bits Therein," U.S. Patent Application Serial No. 09/747,574, filed December 22, 2000; inventors: Thomas H. Lee, James M. Cleeves, and Mark G. Johnson.
	A29	"Method for Deleting Stored Digital Data from Write-Once Memory Device," U.S. Patent Application Serial No. 09/638,439, filed August 14, 2000; inventors: Christopher S. Moore, Derek J. Bosch, Daniel C. Steere, and L. James Tringali.
pmc	A30	"Solid-State Memory Device Storing Program Code and Methods for Use Therewith," U.S. Patent Application Serial No. 09/775,745, filed February 2, 2001; inventors: Christopher S. Moore, Roger March, Dan Brown.

EXAMINER 	DATE CONSIDERED 2/28/05
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.